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SPEC CPU2017 Command Lines - Uweb.engr.arizona.edu

1. A. Limaye, And T. Adegbija, \A Workload Characterization Of The SPEC CPU2017 Benchmark Suite," In Proc. 2018 International Symposium On Performance Analysis Of Systems And Software (ISPASS), April 2018, Pp. 149-158. [link]. Integer Benchmarks Floating Point Benchmarks Rate Speed Rate Speed 500.perlbench R 600.perlbench S 503.bwaves R 603 ... May 12th, 2024

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Works Cited - Engr.psu.edu

Works Cited Model Codes And Standards: International Code Council, International Building Code, 2003. Illinois: International Code Council Publications, Sixth Printing December 2004. American Society Of Civil Engineers, ASCE Standard 7-05 Minimum Design Loads For May 4th, 2024

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January 13-17, 2019, Washington, D.C., USA (presentation). 11. Guo, Y., And Van De Lindt, J.W. (2017). "Data-driven Simulation Of Hurricane Wind Field Based On An Asymmetric Holland Model", The National Institute Of Standards And Technology Center Of Exce Apr 23th, 2024

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Hennessy, Computer Organization And Design, Morgan Kaufmann Publishers, 1997. X1 Getting Started The MIPS Architecture Is An Example Of A Reduced Instruction Set Computer Architecture, Which Is Characterized By A Relatively Small Set Of Instructions And A Comparativel Mar 12th, 2024

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