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Z. 11 Multiple Processes, Concurrency 21

Eqproc:process (a, B, C) Begin 0 End Process Eqproc;

Eqproc:process (a, D, E) Begin 0 End Process Eqproc;

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Coding TechniquesContrasting Their Difference. Index

Terms — VHDL Code, Verilog Code, Finite State

Machine, Mealy Machine, Moore Machine, Modeling

Issues, State Encoding. I. INTRODUCTION The

Automata Theory Is The Basis Behind The Traditional

Model Of Computation And Is Used For Many Purposes

Other Mar 7th, 2024VHDL Design And Synthesis Of PCI

Express Bus ControllerFig.1 PCI Express X2 Connection

III. INTRODUCED ARCHITECTURE The Proposed

Architecture For PCIe Of 64 Bit Is Shown In The Figure

2. The Major Parts Of The Architecture Are The CPU,

CCU, Peripherals Devices Such As Keyboard, Keypad,

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