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Manipulate Any Data. BCF 03h,5 This Instruction Does The Opposite Of BSF. It Means “Bit Clear F”. The Two Numbers That Follow Are The Address Of The Register, In This Case The STATUS Register, And The Bit Number, In This Case Bit 5. So What We Have Done Now Is Set Bit 5 On Our STAUS Register To 0 We Are Now Back In Bank 0. Apr 1th, 2024

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- The Instruction Tell The CPU What To Do, While Directives (pseudo Instructions) Give Direction To Assembler. - Directives: EQU, SET, ORG (Origin), END, LIST - Directive Don't Generate Any Machine Code And Used By Assembler. EQU (equate) O It Is Used To Define A Constant Value Or A Fixed Address. Apr 1th, 2024

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