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Condition Mainly Used With Binary Logic Elements Where The Logic State 1 (TRUE) Is Converted To A Logic State 0 (FALSE) Or Vice Versa [IEC 60617-12, IEC 61082-2] 3.20 Logic Inversion Condition Mainly Used With Binary Logic Elements Where A Higher Physical Level Is Converted To A Lower Physical Level Or Vice Versa [Jan 2th, 2024

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An Introduction To Neural Networks-Kevin Gurney 2018-10-08 Though Mathematical Ideas Underpin The Study Of Neural Networks, The Author Jan 5th, 2024

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Sensor Type Semiconductor Response Time \square 30 S Resume Time \square 30 S Working Voltage DC (5 ± 0.1) V Working Current \square 150 MA 10-1000PPB Solution 10PPB Alarm Point 500PPb±200PPb Lifespan 5 Years Working Conditions Temp. \square -10 \square 55 °C Humidity \square 20% \square 90% RH Storage Conditions Temp. \square -20 \square 60 °C Mar 8th, 2024

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