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M. Morris Mano, Computer System Architecture, 3 Edition, Prentice Hall, 1992. C. M-B. Lin, Digital System Designs And Practices: Using Verilog HDL And FPGAs, Wiley, 2008. 2013-2014 University Of Miami Academic Bulletin Desc Apr 5th, 2024

## **DESIGN OF VENDING MACHINE USING VERILOG HDL**

The Finite State Machine (FSM) Approach Is Adopted For The Design Of Vending Machine. The Design Is Achieved By Formulating The Verilog Code For The FSM-based Machine Using Behavioural Modeling And Simulating The Testbench For Three Products Using Xilinx ISE Tool. Keywords:- Vending Machine, Apr 1th, 2024

## **NU FSM IT Staff, NU FSM System / Application ...**

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## **Appendix: Verilog HDL Design**

Definition, A High-level Language Such As C Or Cpp Or Verilog Behavior-level Description Is Widely Used In This Stage. Figure A.3 High-level Jan 8th, 2024

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