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(7400, 7402, 7404, 7408, 7432, 7486) BRIEF THEORY: AND Gate: The AND Operation Is Defined As The Output As One If And Only If All The Inputs Are One. 7408 Is The Two Input AND Gate IC.A&B Are The Input Terminals &Y Is The Output Terminal. $Y = A.B$ Mar 2th, 2024

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Lecture 3: Basic Logic Gates & Boolean Expressions Professor Peter Cheung
Department Of EEE, Imperial College London (Floyd 3.1-3.5, 4.1) ... Timing Diagram
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Final Zeros Are Significant If There Is A Decimal Present. For Example 200kg. And 2,500kg. Have Three And Four Significant Figures, Respectively. 6. Final Zeros After A Decimal Are Significant. For Example, The Numbers 2.50L And 3.777L Have Three And Five Significant Figures 0 Respec Apr 16th, 2024

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