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8086 16-BIT HMOS MICROPROCESSOR 8086 8086-2 8086-1 Will Release The Local Bus During T4 Of The Cycle When All The Following Conditions Are Met:

1. Request Occurs On Or Before T2.
2. Current Cycle Is Not The Low Byte Of A Word (on An Odd Address).
3. Current Cycle Is Not The First Acknowledge Of An Interrupt Acknowledge Sequence.
4. A Locked Instruction Is Not Currently Executing.

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MM/M1/LU3/V1/2004 2 • It Requires Single Phase Clock

With 33% Duty Cycle To Provide Internal Timing. •

8086 Is Designed To Operate In Two Modes, Minimum

And Maximum. • I Feb 1th, 2024.

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Microprocessor Had Major Improvement Over The

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