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Computer Architecture: Dataflow (Part I) Computer," CACM 1985. ! Arvind And Nikhil, "Executing A Program On The MIT Tagged-Token Dataflow Architecture," IEEE TC 1990. ! Patt Et Al., "HPS, A New Microarchitecture: Rationale And Introduction," MICRO 1985. ! Lee And Hurson, "Dataflow 19th, 2024.

Bebop: A Path-sensitive Interprocedural Dataflow Engine  
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