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SYSTEMS Using ...Solution: (a) Instruction Cycle Time Is
 $1/(4 \text{ MHz}) = 250 \text{ Ns}$ (b) Instruction Cycle Time Is $1/(8$
 $\text{ MHz}) = 125 \text{ Ns}$ (c) Instruction Cycle Time Is $1/(10 \text{ MHz})$
 $= 100 \text{ Ns}$ Example 8-1. Fuse Bits And Reset Delay The
Most Difficult Time For A System Is During Power-up.
The CPU Needs May 20th, 2024

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