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Anurag R. Katiyar Education

Exceptional Problem Solving Skills, Good Teams Skills Work Experience Microsoft Corporation Redmond, WA SDET Intern May. 2007 - Aug. 2007 { Responsible For The Design And Implementation Of Component And Licensing Veri Cation Utility For The Upcoming WIN7 Release. Symantec S/W India Ltd. Pune, India Senior Feb 4th, 2024

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Anurag Khandelwal C-307,LLR Hall Of Residence, IIT Kharagpur Phone: +91-9051325740 Email: Anuragkhandelwal.iit@gmail.com Education SCHOLASTIC ACHIEVEMENTS Undergraduate Studies B. Tech (Hons.), Computer Science And Engineering CGPA: 9.66/10 (at The End Of 6th Semester). Department Rank 1 (out Of 88 Students May 8th, 2024

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Ph.D. In Electrical Engineering And Computer Science GPA: 5.00/5.00 MIT Presidential Fellowship University Of California, Berkeley August 2013 - May 2017 B.S. In Electrical Engineering And Computer Science GPA: 3.95/4.00 Graduated With Highest Honors (top 3%) Experience Improbable Mar 1th, 2024

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Feb 16, 2013 · Senior Economist, 2011-2017 Economist, 2004-2011 Intern, Office Of The First Deputy Managing Director, 2001-2002 Goldman Sachs, 2018- 2020 Managing Director, Global Macro Research, And India Chief Economist Reserve Bank Of Feb 6th, 2024

Samarth Mishra

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