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(0 To 72.5 Psi) 0.2 To 4.7 Volts Output PIN NUMBER NOTE: Pins 4, 5, And 6 Are Internal Device Connections. Do Not Connect To External Circuitry Or Ground. Pin 1 Is Noted By The Notch In The Lead. MPX5500D Jan 19th, 2024

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Semiconductor Processing And Device Simulation Using ...[1] Plummer, Deal, Griffin, "Silicon VLSI Technology: Fundamentals, Practice, And Modeling (3rd Edition)", Prentice Hall Inc 2000. [2] Design Assignment Of EE 129D And Lecture Notes Of EE 222 From Prof. Chui, UCLA [3] Lecture Notes Of EE 122L From Prof. Candler, UCLA, [4] Lecture Jan 29th, 2024

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